

configuring the device to determine an estimated power margin according to at least one of the total bit rate and the target bit rate;

configuring the device to replace the power margin with the estimated power margin; and

configuring the device to repeat the first loop of determining the bit loading capacities and the power modification factors, determining the total bit rate, determining the estimated power margin, and replacing the power margin, for a predetermined number of times, until the total bit rate approximates or equals to the target bit rate, or until obtaining two total bit rates that bound the target bit rate between the two total bit rates.

REMARKS

By this Preliminary Amendment, Applicants have added new claims 20-100 to this reissue application. Accordingly, claims 1-100 are pending in this reissue application.

Applicants respectfully submit that no new matter has been added by these new claims. In particular, support for claims 20-38 may be found, for example, in col. 11, line 49 through col. 16, line 52. Support for claims 39-48 may be found, for example, in col. 16, line 53 through col. 20, line 3. Support for claims 49-58 may be found, for example, in col. 11, line 49 through col. 16, line 52. Support for claims 59-77 may be found, for example, in col. 11, line 3 through col. 16, line 52 and col. 20 ll. 4-57. Support for claims 78-87 may be found, for example, in col. 11, ll. 3-48 and col. 16, line 53 through

FINNEGAN
HENDERSON
FARABOW
GARRETT &
DUNNER ^{LLP}

1300 I Street, NW
Washington, DC 20005
202.408.4000
Fax 202.408.4400
www.finnegan.com

Reissue Application of
U.S. Patent No. 6,292,515

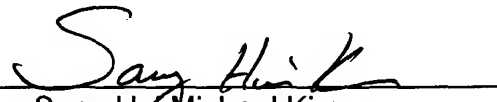
col. 20, line 57. Support for claims 88-100 may be found, for example, in col. 11, line 3 through col. 16, line 52 and col. 20 ll. 4-57.

If the Examiner believes a telephone interview would expedite the prosecution of this reissue application, the Examiner is invited to contact S.H. Michael Kim at (650) 849-6680. Please charge any additional required fees to Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: September 18, 2003

By: 
Sang Hui Michael Kim
Reg. No. 40,450

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Date: September 18, 2003

Signed: 

Linda Phillips

FINNEGAN
HENDERSON
FARABOW
GARRETT &
DUNNER LLP

1300 I Street, NW
Washington, DC 20005
202.408.4000
Fax 202.408.4400
www.finnegan.com

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